



ALAT

#	VALID	TYPE	REG-ID	ADDRESS
n				
n-1				
n-2				
n-3				
n-4				
n-5				
n-6	0	int	rp60	xxzz
n-7				
:				
:				
:				
2				
1				
0				

520

ld.a r30 <- [r20]
 sub r35 <- r30, r15
 st [r80] <- r45
 chk.a r30

↓ DECODE ↓
 (r30 destination)

ld.a r30 <- [r20]
 sub r35 <- r30, r15
 st [r80] <- r45
 chk.a r30

(r30 source)

↓ REGISTER RENAME ↓

ld.a rp60 <- [rp50]
 sub rp65 <- rp60, rp25
 st [rp85] <- rp55
 chk.a rp60

(rp60 source)

FIG. 5